



# SMT16N65

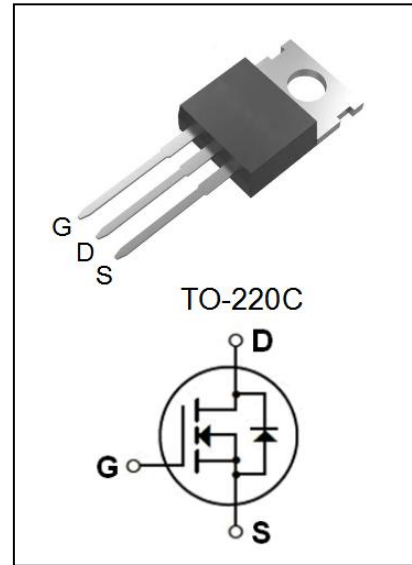
650V N-Channel MOSFET

● **Features:**

- 16.0A, 650V,  $R_{DS(on)(Typ)} = 0.45\Omega @ V_{GS}=10V$
- Low Gate Charge
- Low  $C_{rSS}$
- 100% Avalanche Tested
- Fast Switching
- Improved dv/dt Capability

● **Application:**

- High Frequency Switching Mode Power Supply
- Active Power Factor Correction



**Absolute Maximum Ratings**( $T_c=25^\circ C$  unless otherwise noted)

Symbol	Parameter	Value	Unit
$V_{DSS}$	Drain-Source Voltage	650	V
$I_D$	Drain Current - Continuous( $T_c=25^\circ C$ ) - Continuous( $T_c=100^\circ C$ )	16.0*	A
		10.1*	A
$I_{DM}$	Drain Current -Pulsed (Note1)	64*	A
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy ( Limit Reference Value ) (Note2)	871	mJ
$I_{AR}$	Avalanche Current (Note1)	12.0	A
$E_{AR}$	Repetitive Avalanche Energy (Note1)	30	mJ
dv/dt	Peak Diode Recovery dv/dt (Note3)	4.5	V/ns
$P_D$	Power Dissipation( $T_c =25^\circ C$ ) -Derate above $25^\circ C$	230	W
		1.84	W/ $^\circ C$
$T_j$	Operating Junction Temperature	150	$^\circ C$
$T_{stg}$	Storage Temperature Range	-55 to+150	$^\circ C$

\* Drain Current Limited by Maximum Junction Temperature.

**Thermal Characteristics**

Symbol	Parameter	Max	Unit
$R_{\theta JC}$	Thermal Resistance,Junction to Case	0.54	$^\circ C /W$
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	62.5	$^\circ C /W$



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### Electrical Characteristics(Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	650	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D=250\mu A$ (Referenced to 25°C)	--	0.68	--	V/°C
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=650V, V_{GS}=0V$	--	--	1	$\mu A$
		$V_{DS}=520V, T_c=125^\circ C$	--	--	10	$\mu A$
$I_{GSSF}$	Gate-Body Leakage Current, Forward	$V_{GS}=+30V, V_{DS}=0V$	--	--	100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS}=-30V, V_{DS}=0V$	--	--	-100	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=8.0A$	--	0.45	0.58	$\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=20V, I_D=8.0A$ (Note4)	--	14	--	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	--	2620	--	pF
$C_{oss}$	Output Capacitance		--	201	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	9.0	--	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 325V, I_D = 16A,$ $R_G = 25\Omega$ (Note4,5)	--	38	--	ns
$t_r$	Turn-On Rise Time		--	99	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	149	--	ns
$t_f$	Turn-Off Fall Time		--	98	--	ns
$Q_g$	Total Gate Charge	$V_{DS} = 520V, I_D = 16A,$ $V_{GS} = 10V$ (Note4,5)	--	52	--	nC
$Q_{gs}$	Gate-Source Charge		--	10	--	nC
$Q_{gd}$	Gate-Drain Charge		--	23	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current		--	--	16	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current		--	--	64	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0V, I_S = 16.0A$	--	--	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0V, I_S = 16.0A,$ $dI_F/dt = 100A/\mu s$ (Note4)	--	455	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	4.95	--	$\mu C$

Notes:

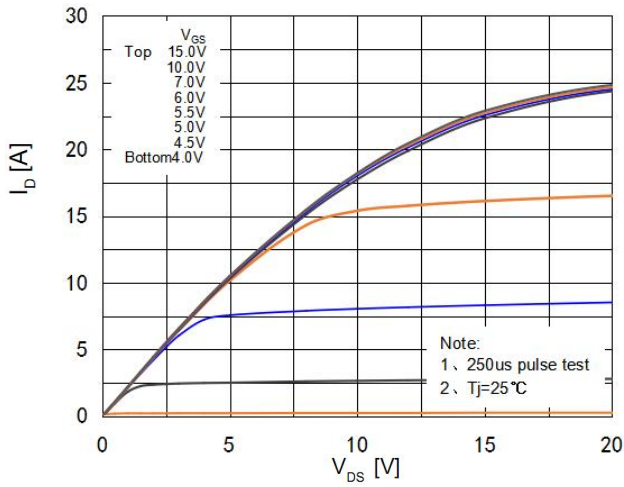
- 1、Repetitive Rating:Pulse Width Limited by Maximum Junction Temperature.
- 2、L = 11mH,  $I_{AS} = 12.0A, V_{DD} = 100V, R_G = 25\Omega$ , Starting  $T_J = 25^\circ C$ .
- 3、 $I_{SD} \leq 16.0A, di/dt \leq 200A/\mu s, V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ C$ .
- 4、Pulse Test : Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
- 5、Essentially Independent of Operating Temperature.



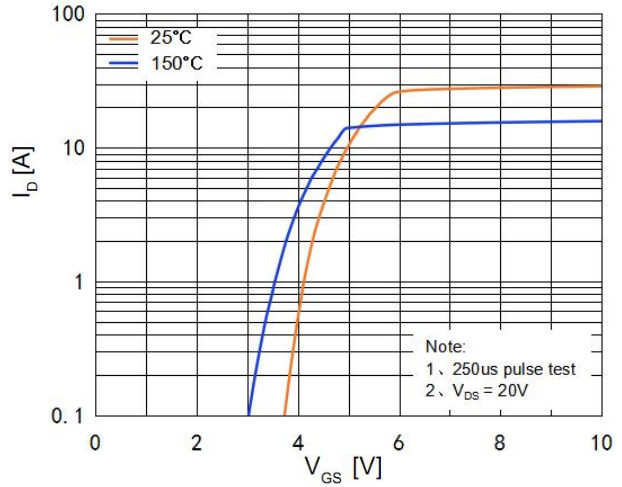
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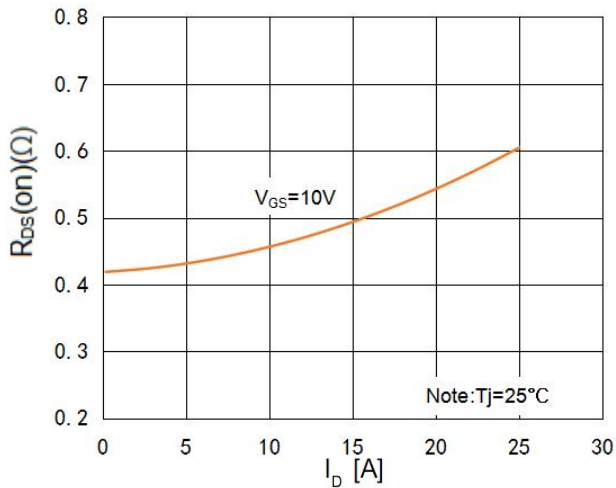
### On-Regin Characteristics



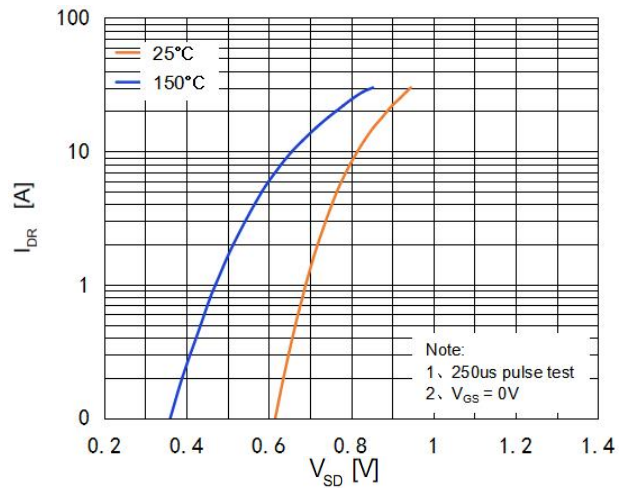
### Transfer Characteristics



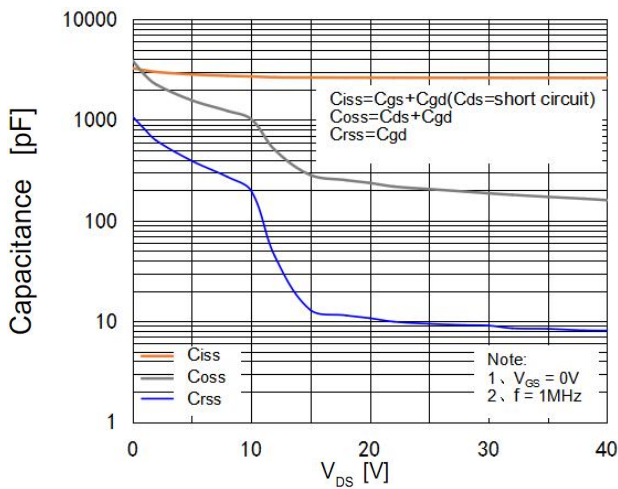
### On-Resistance Variation vs. Drain Current and Gate Voltage



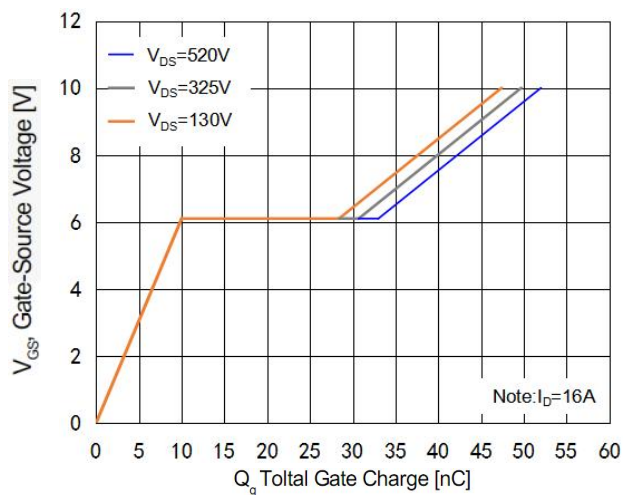
### Body Diode Forward Voltage Variation vs. Source Current and Temperature



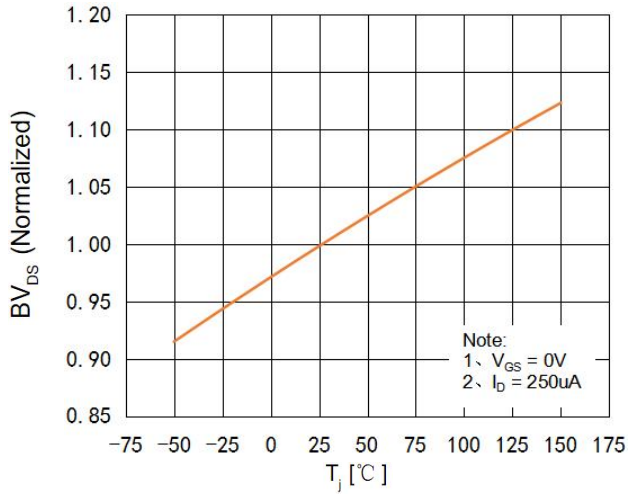
### Capacitance Characteristics



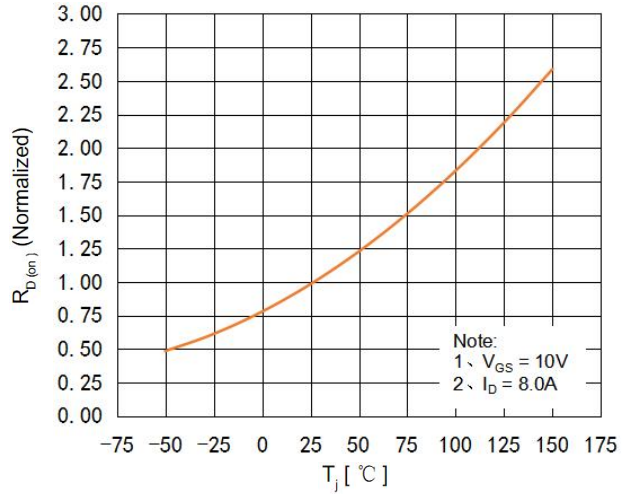
### Gate Charge Characteristics



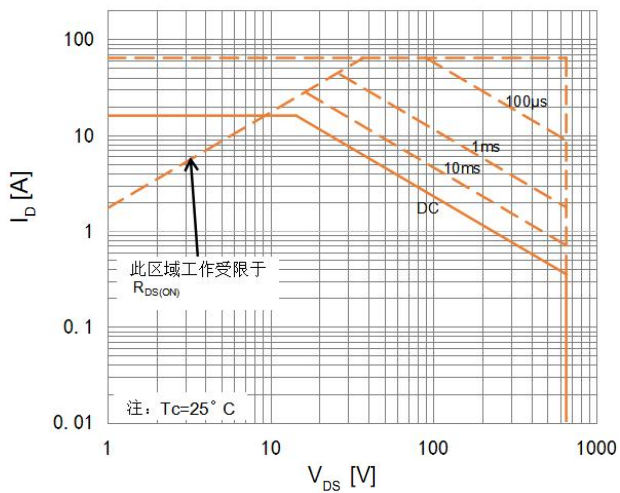
### Breakdown Voltage Variation vs. Temperature



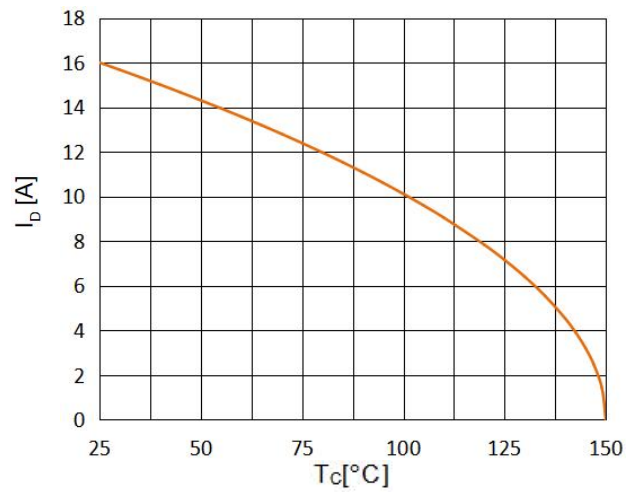
### On-Resistance Variation vs. Temperature



### Maximum Safe Operating Area



### Maximum Drain Current Vs. Case Temperature





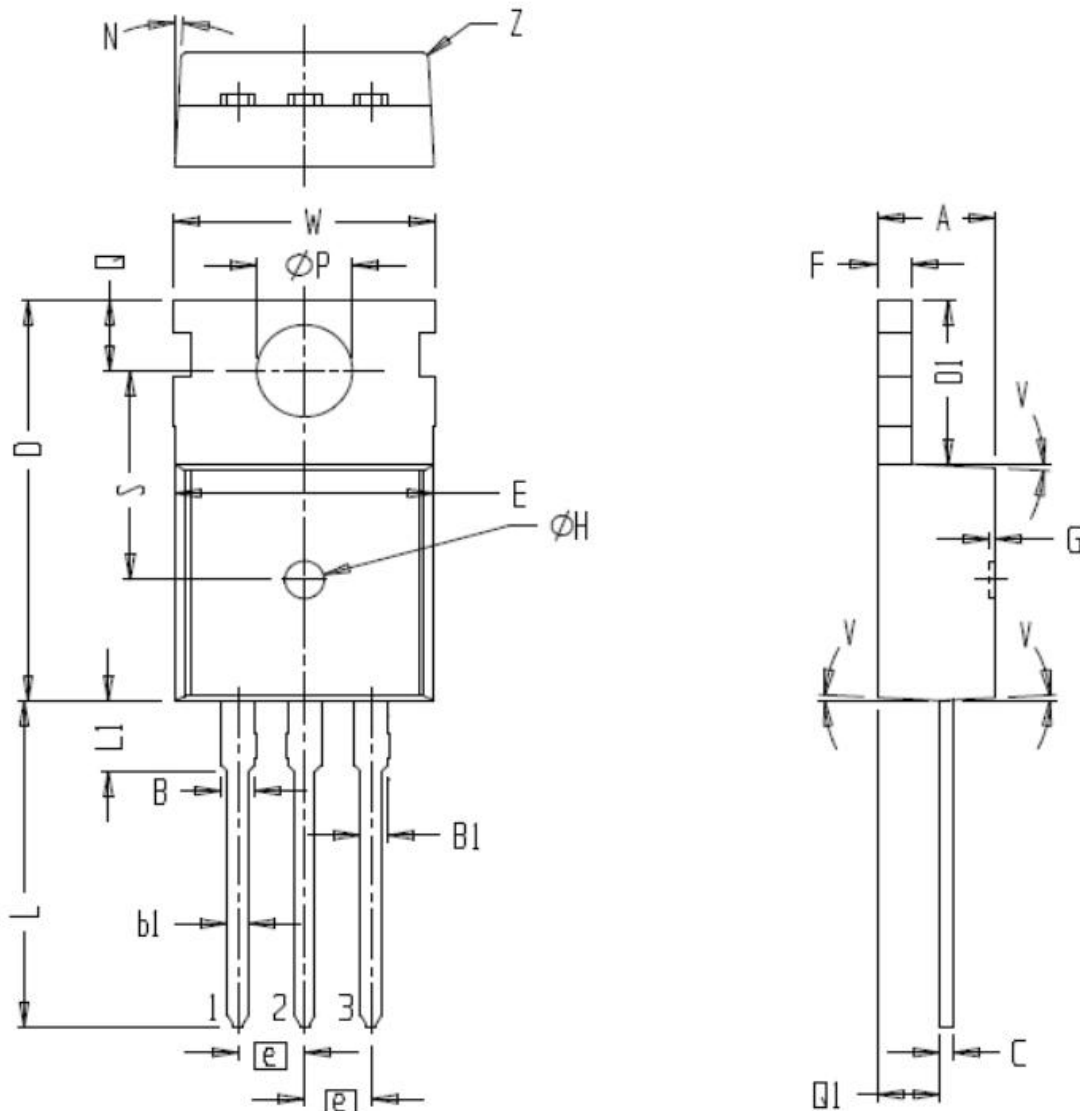
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## TO-220C Package Dimensions

UNIT: mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	4.00	4.40	4.80	E	9.40	9.90	10.40
B	1.17	1.32	1.47	e		2.54	
B1	0.91	1.06	1.21	F	1.15	1.30	1.45
b1	0.65	0.80	0.95	L	12.00	13.00	14.00
c	0.40	0.50	0.60	L1	2.50	3.00	3.50
D	14.90	15.90	16.90	Q	2.30	2.80	3.30
D1	6.10	6.60	7.10	Q1	1.90	2.40	2.90
W	9.50	10.00	10.50	$\phi P$	3.40	3.65	3.90
S		8.30		Z	0		0.20
$\phi H$		1.50		N		3 °	
G		0.10		V		3 °	





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### 注意事项:

- 1、在电路设计时请不要超过器件的最大额定值，否则会影响整机的可靠性。
- 2、MOSFET产品为静电敏感型器件，使用时应注意采取防静电保护措施，如佩戴防静电手环、设备接地等。
- 3、如需安装散热片，请注意控制扭力大小及散热片的平整度。
- 4、该规格书由华科公司制作，并可能不定期更改，恕不另行通知。
- 5、如有疑问，请及时联系我司销售代表。

### 版本履历表:

序号	版本号	修改时间	修改记录
1	V1.0	2022-12-20	首次发行